

Form 1449 (Modified)	Atty Docket No. PETAP002	Application No.: 09/802,434
<b>Information Disclosure Statement By Applicant</b>	Applicant: Kuo et al.	
(Use Several Sheets if Necessary)	Filing Date March 9, 2001	Group 2662

#### U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
hc	A	6,646,983 B1	11/11/03	Roy et al.	—	—	11/21/00
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

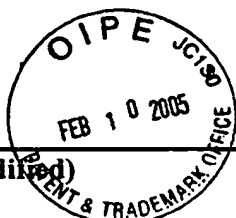
#### Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	J							
	K							
	L							
	M							
	N							

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O	
	P	
	Q	
Examiner <i>[Signature]</i>		Date Considered 5/24/05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

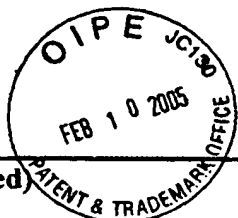


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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
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	H	4,965,788	10/1990	Newman			
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	J	5,544,168	08/1996	Jeffrey et al.			
	K	5,703,879	12/1997	Proctor et al.			
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	S	6,542,507	04/2003	Khacherian et al.			
hc	T	6,747,971	06/2004	Hughes et al.	—	—	
Examiner <i>[Signature]</i>				Date Considered <i>5/24/05</i>			

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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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hc	V	6,603,771	08/2003	Raza, S. Babar	—	—	
hc	W	6,658,016	12/2003	Dai et al.	—	—	

#### Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
hc	X	M. Shreedhar and George Varghese, "Efficient Fair Queuing Using Deficit Round-Robin", IEEE/ACM Transactions on Networking, Vol. 4, No. 3, June 1996.
	Y	Nick McKeown, "iSLIP: The iSLIP Scheduling Algorithm for Input-Queued Switches", IEEE/ACM Transactions on Networking, Vol. 7, No. 2, pp. 188-201, April 1999.
	Z	Kenneth Y. Yun, "A Terabit Multi-Service Switch With Quality Of Service Support", HOT Interconnect 8, pp. 21-30, August 2000.
	A1	Nick McKeown, "Fast Switched Backplane for a Gigabit Switched Router", Department of Engineering, Stanford University, Stanford, CA
	B1	Prabhakar et al., "Multicast Scheduling for Input-Queued Switches", IEEE Journal on Selected Areas in Communications, May 1996.
hc	C1	McKeown et al., "The Tiny Tera: A Packet Switch Core", Hot Interconnects V, Stanford University, August 1996.
Examiner <i>Wong</i>		Date Considered <i>2/24/05</i>

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